

Docket Number: 060188-0124

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Yasuyuki DOI, et al. : Confirmation Number: 4255
Application No.: 10/019,437 : Group Art Unit: 2675
Filed: December 31, 2001 : Allowed: August 01, 2005
Examiner: ALECIA DIANE NELSON
For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND
METHOD FOR CONTROLLING THE SAME(AS AMENDED)

**COMMENTS RESPONSIVE TO STATEMENT OF
REASONS FOR ALLOWANCE
UNDER 37 C.F.R. §104(e)**

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

A Statement of Reasons for Allowance accompanied the August 1, 2005 Notice of Allowability regarding the above-identified application. Entry of that Statement into the record should not be construed as any agreement with or acquiescence by Applicants in the stated reasoning.

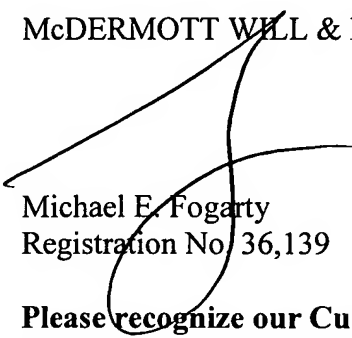
The Examiner stated that Sasaki fails to provide any teaching regarding each buffer connected to each branch reference voltage wire preventing the current from flowing to each branch reference voltage wire. However, more correctly, Sasaki fails to provide any teaching regarding each buffer connected to each branch reference voltage wire preventing the current from flowing via (i.e., to or from) each branch reference voltage wire.

10/019,437

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



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Date: September 28, 2005